

Day : Sunday
Date: 3/11/2007


PALM INTRANET

Time: 15:09:21

Inventor Name Search Result

Your Search was:

Last Name = DEAS

First Name = ALEXANDER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10997591	Not Issued	61	11/24/2004	High speed amplifier incorporating pre-emphasis	DEAS, ALEXANDER R.
10997592	Not Issued	61	11/24/2004	Reference voltage generator for logic elements providing stable and predefined gate propagation time	DEAS, ALEXANDER R.
08525586	Not Issued	166	10/30/1995	FAULT TOLERANT MEMORY SYSTEM	DEAS, ALEXANDER R.
08718370	5896451	150	09/25/1996	ADAPTIVE TELEPHONE INTERFACE	DEAS, ALEXANDER R.
08767393	5721741	250	12/16/1996	MEMORY TEST SYSTEM	DEAS, ALEXANDER R.
08944824	6065090	150	10/06/1997	MEMORY MANAGEMENT APPARTUS THAT REPLACES DEFECTIVE STORAGE LOCATIONS WITH FUNCTIONAL STORAGE LOCATIONS	DEAS, ALEXANDER R.
09424321	Not Issued	161	02/17/2000	MEMORY REDUNDANCY ALLOCATION SYSTEM AND A METHOD OF REDUNDANCY ALLOCATION	DEAS, ALEXANDER ROGER
09477726	Not Issued	161	01/05/2000	MEMORY CHARACTERISATION MEANS AND METHOD	DEAS, ALEXANDER ROGER
09481121	Not Issued	161	01/11/2000	Test Probe Interface Unit And Method Of Manufacturing The Same	DEAS, ALEXANDER ROGER
09659659	6460152	250	09/11/2000	HIGH SPEED MEMORY TEST SYSTEM WITH INTERMEDIATE STORAGE BUFFER AND METHOD OF	DEAS, ALEXANDER ROGER

				TESTING	
<u>09898250</u>	<u>6834255</u>	150	07/03/2001	TIMING CONTROL MEANS FOR AUTOMATIC COMPENSATION OF TIMING UNCERTAINTIES	DEAS, ALEXANDER ROGER
<u>09967535</u>	<u>6820234</u>	150	10/01/2001	SKEW CALIBRATION MEANS AND A METHOD OF SKEW CALIBRATION	DEAS, ALEXANDER ROGER
<u>09985725</u>	Not Issued	161	11/06/2001	Low swing communication system	DEAS, ALEXANDER ROGER
<u>09985726</u>	<u>6480021</u>	150	11/06/2001	TRANSMITTER CIRCUIT COMPRISING TIMING DESKEWING MEANS	DEAS, ALEXANDER ROGER
<u>10012779</u>	<u>6642764</u>	150	12/10/2001	HIGH PRECISION RECEIVER WITH SKEW COMPENSATION	DEAS, ALEXANDER ROGER
<u>10038868</u>	Not Issued	161	01/08/2002	Receiver with automatic skew compensation	DEAS, ALEXANDER ROGER
<u>10079260</u>	<u>7092439</u>	150	02/21/2002	MEANS AND METHOD OF DATA ENCODING AND COMMUNICATION AT RATES ABOVE THE CHANNEL BANDWIDTH	DEAS, ALEXANDER ROGER
<u>10090828</u>	<u>6566245</u>	150	03/06/2002	METHOD OF MANUFACTURING PROBE UNIT AND PROBE UNIT MANUFACTURED USING THIS METHOD	DEAS, ALEXANDER ROGER
<u>10090829</u>	Not Issued	93	03/06/2002	INTERFACE DEVICE WITH STORED DATA ON TRANSMISSION LINES CHARACTERISTICS	DEAS, ALEXANDER ROGER
<u>10151185</u>	<u>7026850</u>	150	05/21/2002	PROGRAMMABLE SELF-CALIBRATING VERNIER AND METHOD	DEAS, ALEXANDER ROGER
<u>10229082</u>	Not Issued	161	08/28/2002	Adaptive equaliser for reducing distortion in communication channel	DEAS, ALEXANDER ROGER
<u>10298047</u>	Not Issued	41	11/18/2002	Latency tolerant processing equipment	DEAS, ALEXANDER ROGER
<u>10383727</u>	Not Issued	161	03/10/2003	Line termination incorporating compensation for device and package parasitics	DEAS, ALEXANDER ROGER

<u>10384266</u>	Not Issued	95	03/10/2003	LINE DRIVER WITH REDUCED POWER CONSUMPTION	DEAS, ALEXANDER ROGER
<u>10387443</u>	Not Issued	161	03/14/2003	Simultaneous bidirectional differential signalling interface	DEAS, ALEXANDER ROGER
<u>10425629</u>	Not Issued	41	04/30/2003	System for communicating with synchronous device	DEAS, ALEXANDER ROGER
<u>10425630</u>	Not Issued	51	04/30/2003	Channel time calibration means	DEAS, ALEXANDER ROGER
<u>10425638</u>	<u>6788102</u>	150	04/30/2003	TRANSMITTER WITH ACTIVE DIFFERENTIAL TERMINATION	DEAS, ALEXANDER ROGER
<u>10425653</u>	Not Issued	161	04/30/2003	Automatic control system for rebreather	DEAS, ALEXANDER ROGER
<u>10425654</u>	<u>6817359</u>	150	04/30/2003	SELF-CONTAINED UNDERWATER RE-BREATHING APPARATUS	DEAS, ALEXANDER ROGER
<u>10730055</u>	Not Issued	30	12/09/2003	Simultaneous bidirectional differential signalling interface	DEAS, ALEXANDER ROGER
<u>10814576</u>	Not Issued	160	04/01/2004	Data encoding and communication at rates above the channel bandwidth	DEAS, ALEXANDER ROGER
<u>10853123</u>	Not Issued	164	05/26/2004	PULL UP FOR HIGH SPEED STRUCTURES	DEAS, ALEXANDER ROGER
<u>10882236</u>	Not Issued	161	07/02/2004	Array oscillator and polyphase clock generator	DEAS, ALEXANDER ROGER
<u>11144625</u>	Not Issued	160	06/06/2005	High speed low dissipation PCB topologies	DEAS, ALEXANDER ROGER
<u>60209613</u>	Not Issued	159	06/06/2000	Test systems for protocol memories	DEAS, ALEXANDER ROGER
<u>60244177</u>	Not Issued	159	10/31/2000	Transceiver with differential termination	DEAS, ALEXANDER ROGER
<u>60244199</u>	Not Issued	159	10/31/2000	Automatic carbon dioxide monitoring life support system and method	DEAS, ALEXANDER ROGER

60251983	Not Issued	159	12/11/2000	High precision receiver with skew compensation	DEAS, ALEXANDER ROGER
60310299	Not Issued	159	08/06/2001	Transmitter circuit comprising timing deskewing means	DEAS, ALEXANDER ROGER
60315907	Not Issued	159	08/28/2001	Means to reduce distortion in a communication channel	DEAS, ALEXANDER ROGER
60331517	Not Issued	159	11/19/2001	Latency tolerant processing equipment	DEAS, ALEXANDER ROGER
60361690	Not Issued	159	03/06/2002	Line driver with reduced power consumption	DEAS, ALEXANDER ROGER
60361691	Not Issued	159	03/06/2002	Line termination incorporating compensation for device and package parasitics	DEAS, ALEXANDER ROGER
60383120	Not Issued	159	05/28/2002	Reference voltage generator for logic elements providing stable and predefined gate propagation time	DEAS, ALEXANDER ROGER
60383121	Not Issued	159	05/28/2002	High speed amplifier incorporating pre-emphasis	DEAS, ALEXANDER ROGER
60383131	Not Issued	159	05/28/2002	Pull up for high speed structures	DEAS, ALEXANDER ROGER
60552723	Not Issued	159	03/15/2004	Eye tracking and expansion using oversampled signals	DEAS, ALEXANDER ROGER
60556971	Not Issued	159	03/23/2004	Means and method of data encoding and communication at rates above the channel bandwidth	DEAS, ALEXANDER ROGER
60558154	Not Issued	159	04/01/2004	Clock recovery in an over-sampled serial communications system	DEAS, ALEXANDER ROGER

[Search and Display More Records.](#)

Search Another: Inventor

Last Name	First Name
DEAS	ALEXANDER

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Sunday
Date: 3/11/2007

Time: 15:09:26


PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = DEAS

First Name = ALEXANDER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>60568237</u>	Not Issued	159	05/06/2004	Clock frequency reduction in communications receiver with coding	DEAS, ALEXANDER ROGER
<u>60584531</u>	Not Issued	159	07/02/2004	Circuit for clock and data recovery	DEAS, ALEXANDER ROGER
<u>60588993</u>	Not Issued	159	07/20/2004	Re-timer circuit for data recovery with fast recovery from a low power mode	DEAS, ALEXANDER ROGER
<u>60599859</u>	Not Issued	159	08/10/2004	Impedance controlled interconnect substrate and package for high-frequency electronic device	DEAS, ALEXANDER ROGER
<u>60600397</u>	Not Issued	159	08/11/2004	Impedance controlled interconnect substrate and package for high-frequency electronic device	DEAS, ALEXANDER ROGER
<u>60602660</u>	Not Issued	159	08/19/2004	Read latency minimisation using frame offset	DEAS, ALEXANDER ROGER
<u>60606127</u>	Not Issued	159	09/01/2004	Digital filter with high speed response	DEAS, ALEXANDER ROGER
<u>08704722</u>	Not Issued	161	09/17/1996	NOISE CANCELLATION SYSTEM AND METHOD	DEAS, ALEXANDER ROGER
<u>08716343</u>	Not Issued	161	10/02/1996	AUTOMATIC SWITCHING INTERFACE	DEAS, ALEXANDER ROGER
<u>08959963</u>	<u>6041422</u>	150	10/24/1997	FAULT TOLERANT MEMORY SYSTEM	DEAS, ALEXANDER ROGER
<u>09016483</u>	Not	161	01/30/1998	MODULE SECURITY DEVICE	DEAS,

	Issued				ALEXANDER ROGER
<u>09101332</u>	6269455	150	07/07/1998	A SYSTEM AND A METHOD FOR PROCESSING INFORMATION ABOUT LOCATIONS OF DEFECTIVE MEMORY CELLS AND A MEMORY TEST APPARATUS WITH DEFECT, COMPRESSION MEANS	DEAS, ALEXANDER ROGER
<u>09304823</u>	Not Issued	161	05/05/1999	SYSTEM AND A METHOD FOR MODELLING MEMORY BUSINESS PARAMETERS	DEAS, ALEXANDER ROGER
<u>09369859</u>	Not Issued	161	08/09/1999	MEMORY TEST SYSTEM WITH A MEANS FOR TEST SEQUENCE OPTIMISATION AND A METHOD OF ITS OPERATION	DEAS, ALEXANDER ROGER
<u>09377172</u>	6366995	250	08/19/1999	SYSTEM AND A METHOD FOR DEFINING TRANSFORMS OF MEMORY DEVICE ADDRESSES	DEAS, ALEXANDER ROGER
<u>09423804</u>	6393543	150	12/05/2000	SYSTEM AND A METHOD FOR TRANSFORMATION OF MEMORY DEVICE ADDRESSES	DEAS, ALEXANDER ROGER
<u>09450693</u>	Not Issued	161	11/30/1999	AFFINE TRANSFORMATION MEANS AND METHOD OF AFFINE TRANSFORMATION	DEAS, ALEXANDER ROGER
<u>09453843</u>	Not Issued	161	12/02/1999	MEMORY REDUNDANCY ALLOCATION SYSTEM AND A METHOD OF REDUNDANCY ALLOCATION	DEAS, ALEXANDER ROGER
<u>09457479</u>	Not Issued	161	12/09/1999	TESTER INTERFACE SYSTEM FOR CURRENT DRIVE MODE MEMORY DEVICE	DEAS, ALEXANDER ROGER

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="DEAS"/>	<input type="text" value="ALEXANDER"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Sunday
Date: 3/11/2007

Time: 15:09:33


PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = ABROSIMOV

First Name = IGOR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10997591	Not Issued	61	11/24/2004	High speed amplifier incorporating pre-emphasis	ABROSIMOV, IGOR A.
10997592	Not Issued	61	11/24/2004	Reference voltage generator for logic elements providing stable and predefined gate propagation time	ABROSIMOV, IGOR A.
09898250	6834255	150	07/03/2001	TIMING CONTROL MEANS FOR AUTOMATIC COMPENSATION OF TIMING UNCERTAINTIES	ABROSIMOV, IGOR ANATOLIEVICH
09985726	6480021	150	11/06/2001	TRANSMITTER CIRCUIT COMPRISING TIMING DESKEWING MEANS	ABROSIMOV, IGOR ANATOLIEVICH
10012779	6642764	150	12/10/2001	HIGH PRECISION RECEIVER WITH SKEW COMPENSATION	ABROSIMOV, IGOR ANATOLIEVICH
10038868	Not Issued	161	01/08/2002	Receiver with automatic skew compensation	ABROSIMOV, IGOR ANATOLIEVICH
10066775	Not Issued	161	02/06/2002	Data processing system	ABROSIMOV, IGOR ANATOLIEVICH
10079260	7092439	150	02/21/2002	MEANS AND METHOD OF DATA ENCODING AND COMMUNICATION AT RATES ABOVE THE CHANNEL BANDWIDTH	ABROSIMOV, IGOR ANATOLIEVICH
10090815	Not Issued	164	07/11/2002	TIMING DELAY GENERATOR AND METHOD USING TEMPERATURE STABILISATION	ABROSIMOV, IGOR ANATOLIEVICH
10090829	Not Issued	93	03/06/2002	INTERFACE DEVICE WITH STORED DATA ON TRANSMISSION LINES CHARACTERISTICS	ABROSIMOV, IGOR ANATOLIEVICH

<u>10151186</u>	<u>6741095</u>	150	05/21/2002	DATA TRANSMISSION SYSTEM, CIRCUIT AND METHOD	ABROSIMOV, IGOR ANATOLIEVICH
<u>10225528</u>	Not Issued	164	08/22/2002	MEMORY TEST APPARATUS AND METHOD OF TESTING	ABROSIMOV, IGOR ANATOLIEVICH
<u>10229082</u>	Not Issued	161	08/28/2002	Adaptive equaliser for reducing distortion in communication channel	ABROSIMOV, IGOR ANATOLIEVICH
<u>10298047</u>	Not Issued	41	11/18/2002	Latency tolerant processing equipment	ABROSIMOV, IGOR ANATOLIEVICH
<u>10383727</u>	Not Issued	161	03/10/2003	Line termination incorporating compensation for device and package parasitics	ABROSIMOV, IGOR ANATOLIEVICH
<u>10384266</u>	Not Issued	95	03/10/2003	LINE DRIVER WITH REDUCED POWER CONSUMPTION	ABROSIMOV, IGOR ANATOLIEVICH
<u>10387443</u>	Not Issued	161	03/14/2003	Simultaneous bidirectional differential signalling interface	ABROSIMOV, IGOR ANATOLIEVICH
<u>10425629</u>	Not Issued	41	04/30/2003	System for communicating with synchronous device	ABROSIMOV, IGOR ANATOLIEVICH
<u>10425630</u>	Not Issued	51	04/30/2003	Channel time calibration means	ABROSIMOV, IGOR ANATOLIEVICH
<u>10656143</u>	<u>6806817</u>	150	09/08/2003	MEANS AND METHOD OF DATA ENCODING AND COMMUNICATION AT RATES ABOVE THE CHANNEL BANDWIDTH	ABROSIMOV, IGOR ANATOLIEVICH
<u>10730055</u>	Not Issued	30	12/09/2003	Simultaneous bidirectional differential signalling interface	ABROSIMOV, IGOR ANATOLIEVICH
<u>10814576</u>	Not Issued	160	04/01/2004	Data encoding and communication at rates above the channel bandwidth	ABROSIMOV, IGOR ANATOLIEVICH
<u>10853123</u>	Not Issued	164	05/26/2004	PULL UP FOR HIGH SPEED STRUCTURES	ABROSIMOV, IGOR ANATOLIEVICH
<u>10882236</u>	Not Issued	161	07/02/2004	Array oscillator and polyphase clock generator	ABROSIMOV, IGOR ANATOLIEVICH
<u>11144625</u>	Not Issued	160	06/06/2005	High speed low dissipation PCB topologies	ABROSIMOV, IGOR ANATOLIEVICH
<u>60209613</u>	Not Issued	159	06/06/2000	Test systems for protocol memories	ABROSIMOV, IGOR ANATOLIEVICH
<u>60216468</u>	Not Issued	159	07/06/2000	Subsystem with stored data on transmission characteristics	ABROSIMOV, IGOR ANATOLIEVICH
<u>60228115</u>	Not	159	08/28/2000	Timing control means for	ABROSIMOV, IGOR

	Issued			automatic compensation of timing uncertainties	ANATOLIEVICH
<u>60238028</u>	Not Issued	159	10/06/2000	Timing delay generator and method using temperature stabilisation	ABROSIMOV, IGOR ANATOLIEVICH
<u>60244179</u>	Not Issued	159	10/31/2000	Pattern dependent driver	ABROSIMOV, IGOR ANATOLIEVICH
<u>60293052</u>	Not Issued	159	05/21/2001	Data transmission means and methods	ABROSIMOV, IGOR ANATOLIEVICH
<u>60310299</u>	Not Issued	159	08/06/2001	Transmitter circuit comprising timing deskewing means	ABROSIMOV, IGOR ANATOLIEVICH
<u>60314496</u>	Not Issued	159	08/24/2001	Memory tester and memory testing system	ABROSIMOV, IGOR ANATOLIEVICH
<u>60315907</u>	Not Issued	159	08/28/2001	Means to reduce distortion in a communication channel	ABROSIMOV, IGOR ANATOLIEVICH
<u>60317216</u>	Not Issued	159	09/06/2001	Self calibrating receiver and method of calibration	ABROSIMOV, IGOR ANATOLIEVICH
<u>60331517</u>	Not Issued	159	11/19/2001	Latency tolerant processing equipment	ABROSIMOV, IGOR ANATOLIEVICH
<u>60361690</u>	Not Issued	159	03/06/2002	Line driver with reduced power consumption	ABROSIMOV, IGOR ANATOLIEVICH
<u>60361691</u>	Not Issued	159	03/06/2002	Line termination incorporating compensation for device and package parasitics	ABROSIMOV, IGOR ANATOLIEVICH
<u>60383120</u>	Not Issued	159	05/28/2002	Reference voltage generator for logic elements providing stable and predefined gate propagation time	ABROSIMOV, IGOR ANATOLIEVICH
<u>60383121</u>	Not Issued	159	05/28/2002	High speed amplifier incorporating pre-emphasis	ABROSIMOV, IGOR ANATOLIEVICH
<u>60383131</u>	Not Issued	159	05/28/2002	Pull up for high speed structures	ABROSIMOV, IGOR ANATOLIEVICH
<u>60431226</u>	Not Issued	159	12/06/2002	High speed low dissipation PCB topologies	ABROSIMOV, IGOR ANATOLIEVICH
<u>60552723</u>	Not Issued	159	03/15/2004	Eye tracking and expansion using oversampled signals	ABROSIMOV, IGOR ANATOLIEVICH
<u>60556971</u>	Not Issued	159	03/23/2004	Means and method of data encoding and communication at rates above the channel bandwidth	ABROSIMOV, IGOR ANATOLIEVICH
<u>60558154</u>	Not Issued	159	04/01/2004	Clock recovery in an over-sampled serial communications system	ABROSIMOV, IGOR ANATOLIEVICH
<u>60568237</u>	Not	159	05/06/2004	Clock frequency reduction in	ABROSIMOV, IGOR

	Issued			communications receiver with coding	ANATOLIEVICH
60588993	Not Issued	159	07/20/2004	Re-timer circuit for data recovery with fast recovery from a low power mode	ABROSIMOV, IGOR ANATOLIEVICH
60602660	Not Issued	159	08/19/2004	Read latency minimisation using frame offset	ABROSIMOV, IGOR ANATOLIEVICH
60606127	Not Issued	159	09/01/2004	Digital filter with high speed response	ABROSIMOV, IGOR ANATOLIEVICH

Inventor Search Completed: No Records to Display.


Search Another: Inventor

Last Name	First Name	
<input type="text" value="ABROSIMOV"/>	<input type="text" value="IGOR"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Sunday
Date: 3/11/2007


PALM INTRANET

Time: 15:09:37

Inventor Name Search Result

Your Search was:

Last Name = COYNE

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name
06337208	4467253	150	01/05/1982	DIFFERENTIAL SIGNAL DECODER	COYNE, DAVID
06829624	4796004	150	02/14/1986	INTEGRATED ANALOG-TO-DIGITAL CONVERTER	COYNE, DAVID
06831565	4748640	150	02/21/1986	DIGITAL CIRCUIT WITH BAND LIMITING CHARACTERISTICS FOR MODEM	COYNE, DAVID
08432744	Not Issued	161	05/02/1995	DISPLAY APPARATUS WITH DATA COMMUNICATION CHANNEL	COYNE, DAVID
08860041	5831837	250	06/09/1997	SWITCH MODE POWER SUPPLY	COYNE, DAVID
10387443	Not Issued	161	03/14/2003	Simultaneous bidirectional differential signalling interface	COYNE, DAVID
10730055	Not Issued	30	12/09/2003	Simultaneous bidirectional differential signalling interface	COYNE, DAVID
60558154	Not Issued	159	04/01/2004	Clock recovery in an over-sampled serial communications system	COYNE, DAVID
60568237	Not Issued	159	05/06/2004	Clock frequency reduction in communications receiver with coding	COYNE, DAVID
60584531	Not Issued	159	07/02/2004	Circuit for clock and data recovery	COYNE, DAVID
60588993	Not Issued	159	07/20/2004	Re-timer circuit for data recovery with fast recovery from a low power mode	COYNE, DAVID
60602660	Not Issued	159	08/19/2004	Read latency minimisation using frame offset	COYNE, DAVID
60606127	Not Issued	159	09/01/2004	Digital filter with high speed response	COYNE, DAVID

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="COYNE"/>	<input type="text" value="DAVID"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)